

## Editorial Contact

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## FPGA Platform Enables Scalable Systems Development in ExpressCard/54 Format

*Card based on Xilinx Virtex-5 FPGA supports embedded, PCIe, and clustered FPGA configurations*

**Seattle, WA – December 2, 2009** – Pico Computing today announced immediate availability of its E-17 development platform, a compact, laptop compatible solution for FPGA development and scalable deployment.

The Pico E-17 platform supports single-lane ExpressCard and PCIe interfaces as well as providing additional general purpose I/O, making it ideal for FPGA algorithm prototyping and deployment in applications ranging from digital signal processing, encryption, image and video processing, to low-latency financial data handling.

"Our customers demand the highest levels of FPGA platform integration, in compact formats," said Mark Hur, Pico Computing Director of Sales and Marketing. "The ExpressCard/54 packaging and range of accessories make the E-17 an ideal platform for applications including signal processing, video, networking, and accelerated computing."

The E-17 card can be configured with a Xilinx Virtex-5 FX70T, SX50T or LX50T FPGA device. Up to 256MB of DDR2 memory and 64MB of Flash memory are available on the card. Board Support Packages provided by Pico Computing allow fast development using Xilinx ISE Design Suite and the Xilinx Embedded Development Kit (EDK).

Reference examples and host development and programming tools are also included. Accessory devices for the E-17 allow its use in standalone mode as well as offering alternate I/O including CameraLink and 1Gbit Ethernet.

For high performance computing applications, up to six Pico FPGA cards can be installed into a PCIe carrier card, and multiple carrier cards can be installed into a single 4U rack mounted chassis to create a scalable FPGA cluster.



*Pico Computing E-17 Card with Xilinx Virtex-5 FX70T FPGA*

## About Pico Computing

Pico Computing, headquartered in Seattle, Washington, specializes in highly integrated development and deployment platforms based on Field Programmable Gate Array (FPGA) technologies. Applications for Pico Computing technologies include cryptography, networking, signal processing, bioinformatics, scientific and financial computing. For more information, visit [www.picocomputing.com](http://www.picocomputing.com).

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