



## Pico Computing Introduces New Products at Supercomputing 2011

*EX-400 M-Series backplane and M-505 Module featuring the Xilinx Kintex-7 series FPGA*

**Seattle, WA – November 14, 2011** – Pico Computing today announces the immediate availability of the EX-400 PCIe backplane. The EX-400, along with the recently released M-505, will be on display during the Supercomputing Conference being held in Seattle, WA, November 12<sup>th</sup> – 18<sup>th</sup> 2011 (booth #2300).

The **EX-400 PCIe backplane** is a fully switched PCIe modular solution, providing x8 Gen2 interface to four M-501/505s or two M-503/504s FPGA modules. With a shorter form-factor than the existing EX-500, this new board can fit into any size chassis. Add multiple EX-400s to your system and build a scalable FPGA cluster.

The **M-505 Module** is the latest in Pico's line of M-Series modular FPGA cards for High-Performance Computing and embedded applications. This board features the Xilinx Kintex-7 K325T FPGA. Based on their 28nm fabrication process, the Kintex-7 achieves a 2x price/performance improvement while consuming 50% less power than previous generation FPGAs. The M-505 has one DDR3 SODIMM for up to 4GB of local RAM, x8 Gen2 PCIe, and 128MB of configuration FLASH. There are 34 LVDS and 8 GTX transceivers available for user I/O.

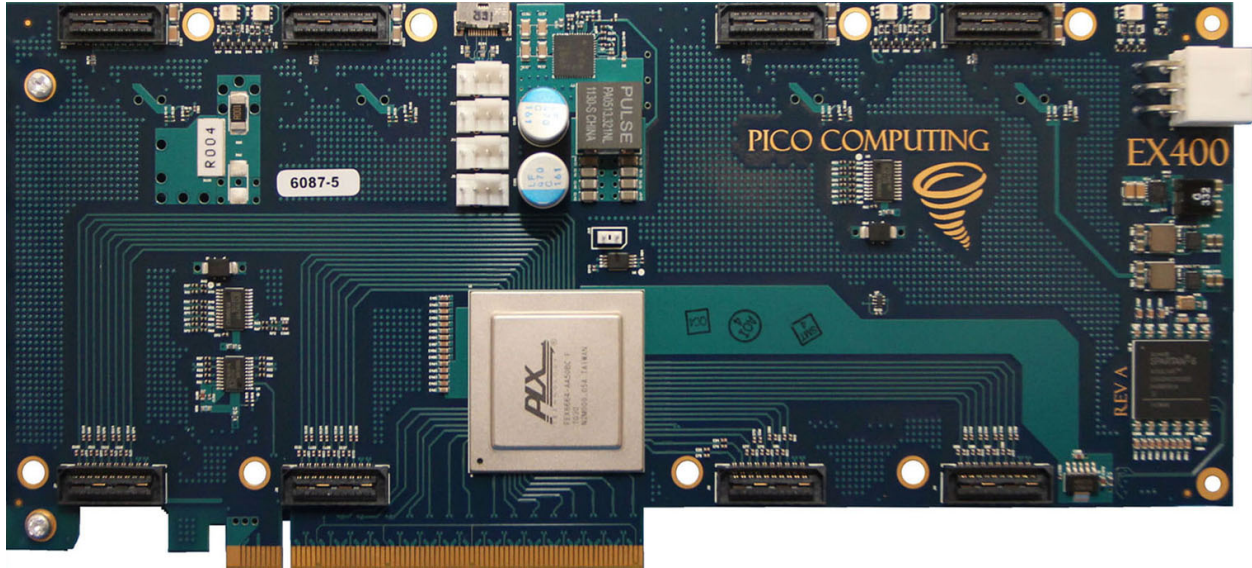
### **About Pico Computing**

Pico Computing, headquartered in Seattle, Washington, specializes in highly integrated development and deployment platforms based on Field Programmable Gate Array (FPGA) technologies. Applications for Pico Computing technologies include cryptography, networking, signal processing, bioinformatics, and scientific computing. Pico Computing products are used in embedded systems as well as in military, national security and high performance computing applications. For more information about Pico products and services, visit [www.picocomputing.com](http://www.picocomputing.com).

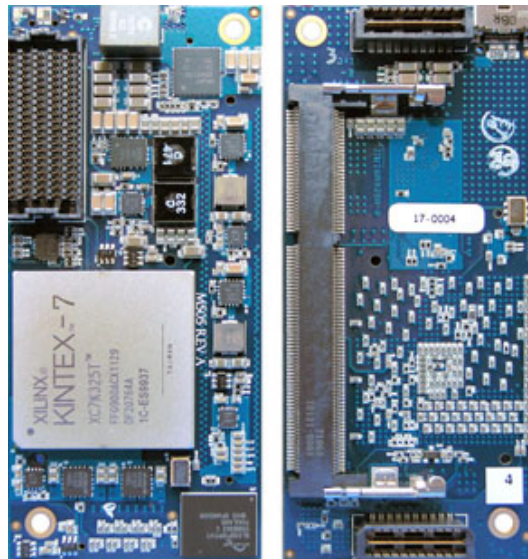
### **Editorial Contact**

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*EX-400, shown without FPGA Modules*



*M-505, top and bottom (left to right)*