

Industry's First Virtex-5 ExpressCard



Xilinx Virtex-5 LX50. The 1.0V, 65nm triple-oxide process increases performance and lowers power consumption.

RoHS Compliant

ExpressCard/34mm
(75mm x 34mm x 5mm)

PLX PEX8311 PCI Express bridge provides complete PCI Express interface capable of speeds up to 2.5 Gbps.

32MB PSRAM provides lower power consumption.

E-16 LX50

Specifications

- Xilinx Virtex-5 LX50
- ExpressCard/34 (75mm x 34mm x 5mm)
- PLX PEX 8311 PCI Express Bridge
- 32MB PSRAM
- 18 Single-Ended or 9 LVDS Lines
- Internal JTAG for debugging
- Stainless Steel Case
- Temperature Range: 0^o to 70^o C

Applications

- Application On Card (AOC) systems. Applications packaged with the platform that they run on.
- Image Processing
- Algorithm Acceleration
- Hybrid embedded processor / DSP applications
- Encryption / Decryption
- Security algorithms and testing
- Software Defined Radio (SDR)
- Embedded control systems
- Weight and size constrained environments such as UAVs, surveillance systems and environmental monitoring devices
- Complete development environment for laptop computers. Ideal for rapid prototyping and classroom environments.

Software

- PicoUtil: Utility program for FPGA management (Windows and Linux)
- Xilinx standard tool set support (ISE, EDK, and Platform SDK)
- ImpulseC CoDeveloper Platform Support Package
- Starbridge Systems' Viva System Description File
- Plug-in for MATLAB/Simulink available

FPGA Specification

- Model: XC5VLX50
- DSP48E Slices: 48
- Logic Cells: 46,080
- CLB Flip Flops: 28,800
- Block RAM: 1,728 Kbits

